APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/533,506	05/02/2005	Bijo Thomas	IN 020004	4348

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PHILIPS INTELLECTUAL PROPERTY & STANDARDS
P.O. BOX 3001
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EXAMINER
PHAN, DEAN

ART UNIT	PAPER NUMBER
2182	

MAIL DATE	DELIVERY MODE
04/27/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action Before the Filing of an Appeal Brief	Application No.	Applicant(s)
	10/533,506	THOMAS, BIJO
	Examiner	Art Unit
	Dean Phan	2182

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED <u>05 April 2007</u> FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☒ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

    a) ☒   The period for reply expires <u>3</u> months from the mailing date of the final rejection.

    b) ☐   The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

        Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

<u>NOTICE OF APPEAL</u>

2. ☐ The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

<u>AMENDMENTS</u>

3. ☐   The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will <u>not</u> be entered because

    (a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);

    (b) ☐ They raise the issue of new matter (see NOTE below);

    (c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or

    (d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

        NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).

5. ☐ Applicant's reply has overcome the following rejection(s): _____.

6. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

7. ☒   For purposes of appeal, the proposed amendment(s): a) ☒ will not be entered, or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

    The status of the claim(s) is (or will be) as follows:

    Claim(s) allowed: _____.

    Claim(s) objected to: _____.

    Claim(s) rejected: <u>1-11</u>.

    Claim(s) withdrawn from consideration: _____.

<u>AFFIDAVIT OR OTHER EVIDENCE</u>

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will <u>not</u> be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).

9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will <u>not</u> be entered because the affidavit or other evidence failed to overcome <u>all</u> rejections under appeal and/or appellant fails to provide a showing a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).

10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

<u>REQUEST FOR RECONSIDERATION/OTHER</u>

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because: <u>see attached.</u>

12. ☐ Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). _____

13. ☐ Other: _____.

KIM HUYNH
SUPERVISORY PATENT EXAMINER
4/20/07

Applicant argues the feature of "**repetition of the identical address** will trigger mapping to a different memory unit", note this is not a claimed limitation. Instead, the claim recites "**detect repetition of a period of an address pattern**". An **address pattern** is not the same as an identical **address**.

Humphrey teaches "**detect repetition of a period an address pattern**". Humphrey uses 24-bit bus to locate a memory location. The upper 4-bit part is used to identify the address of a a pariticular memory bank (an address pattern). Humphrey's comparators repeatedly detect 4-bit address patterns. During the detection, if the 4-bit address pattern matches, the address is directed to the same memory bank. If the 4-bit address pattern does not match, the address is switched to the other memory bank. Therefore, Humphrey teaches "**detect a repetition of a period of an address pattern**" to switch to other memory bank.

Regarding to the "**identical addresses**... alternately **map to different ones of the memory units**..."As explained above, Humphrey uses 24-bit bus to locate a memory location. The upper 4-bit part is used to identify a pariticular memory bank. The 20-bit part is used as memory addresses of the associated memory bank identified by the 4-bit part. For the same location on each memory bank, the location is identified by an identical 20-bit address. Humphrey's comparator detects the difference of two 4-bit parts to switch between the memory banks. Thus, **identical 20-bit addresses ...** alternately **map to different ones of the memory units** based on the 4-bit parts.